

21.6 Information Structure Mapping Requirements

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This section identifies the normative requirements established for mapping FX logical information structures to Phase 0 IDL structures and generated types. The canonical requirement statements are maintained in [Annex D: Requirements, Part 4, Section 21.6](#).

Table 21-5: Information structure mapping requirements.

Requirement ID	Statement
P4-REQ-21-6-001	The Phase 0 Implementation Profile / PSM SHALL map selected FX logical information structures to IDL structures and generated types.
P4-REQ-21-6-002	Each IDL structure SHALL identify the FX logical information structure it realises.
P4-REQ-21-6-003	Each generated type SHALL identify the IDL structure from which it derives.
P4-REQ-21-6-004	Each IDL and generated type mapping SHALL identify the Runtime Plane or Runtime Planes associated with the information structure.
P4-REQ-21-6-005	Each IDL and generated type mapping SHALL identify the DDS Topic or Topics that carry the generated type.
P4-REQ-21-6-006	Each IDL and generated type mapping SHALL identify the producing and consuming FX logical Nodes or Node Roles.
P4-REQ-21-6-007	Each IDL and generated type mapping SHALL identify the source IDL file.
P4-REQ-21-6-008	Each IDL and generated type mapping SHALL identify the applicable generation rule.
P4-REQ-21-6-009	Each IDL and generated type mapping SHALL identify the applicable versioning and compatibility expectations.
P4-REQ-21-6-010	Each IDL and generated type mapping SHALL preserve traceability to the Part 3 FX logical information structure.
P4-REQ-21-6-011	IDL structures SHALL NOT redefine FX logical information structures.
P4-REQ-21-6-012	Generated types SHALL NOT redefine IDL structures or FX logical information structures.

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