

21.2 Phase 0 Profile Requirements

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This section identifies the normative requirements established for the Phase 0 Implementation Profile / PSM. The canonical requirement statements are maintained in [Annex D: Requirements, Part 4, Section 21.2](#).

Table 21-1: Phase 0 profile requirements.

Requirement ID	Statement
P4-REQ-21-2-001	The Phase 0 Implementation Profile / PSM SHALL define a phase-specific implementation baseline for the FX Demo.
P4-REQ-21-2-002	The Phase 0 Implementation Profile / PSM SHALL map selected Part 3 FX Demo Logical Profile elements to Phase 0 implementation artefacts.
P4-REQ-21-2-003	The Phase 0 Implementation Profile / PSM SHALL preserve traceability to the Part 1 Conceptual Architecture, the Part 2 Distributed Node-Based Logical Architecture / PIM, and the Part 3 FX Demo Logical Profile.
P4-REQ-21-2-004	The Phase 0 Implementation Profile / PSM SHALL NOT redefine Part 1 conceptual elements, Part 2 logical elements, or Part 3 FX logical profile elements.
P4-REQ-21-2-005	The Phase 0 Implementation Profile / PSM SHALL distinguish implementation artefacts from logical elements.
P4-REQ-21-2-006	The Phase 0 Implementation Profile / PSM SHALL define the implementation mappings needed for the Phase 0 implementation baseline.
P4-REQ-21-2-007	The Phase 0 Implementation Profile / PSM SHALL NOT define the final implementation architecture for all later development phases.
P4-REQ-21-2-008	A later implementation profile MAY extend, refine, supersede, or replace the Phase 0 Implementation Profile / PSM while preserving traceability to the applicable conceptual, logical, FX logical, implementation, and Developer Handbook artefacts.

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