

8. Mapping from FX Logical Nodes to Implementation Artifacts

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This section defines how selected FX Demo logical elements are mapped to the Phase 0 implementation baseline. The mappings identify how FX logical Nodes, Node Roles, Communication Endpoints, information structures, Runtime Planes, and related architectural elements are realized by Phase 0 implementation artifacts. These mappings preserve the distinction between the logical architecture and the implementation profile by identifying implementation realizations without changing the meaning of the underlying logical elements. They also provide the implementation traceability needed for subsequent development, deployment, testing, observation, and evidence activities.

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