

3. Traceability to Parent and Source Architectures

[Go To Top](#)

This section describes how the Phase 0 Implementation Profile / Platform-Specific Model (PSM) preserves [traceability](#) to the architectural sources that govern its implementation decisions. It identifies the relationship between the Phase 0 implementation profile and the parent structured-information-processing architecture, the financial-domain reference architecture, the conceptual architecture, the platform-independent logical architecture, the FX Demo Logical Profile, the selected Phase 0 implementation source material, and the Phase 0 Developer Handbook.

The following sections describe these traceability relationships:

- [3.1 Traceability to SIP-RA](#)
- [3.2 Alignment with FDIS-RA](#)
- [3.3 Traceability to Part 1 Conceptual Architecture](#)
- [3.4 Traceability to Part 2 Logical Architecture / Platform-Independent Model \(PIM\)](#)
- [3.5 Traceability to Part 3 FX Demo Logical Profile](#)
- [3.6 Traceability to Phase 0 Implementation Source Material](#)
- [3.7 Traceability to Phase 0 Developer Handbook](#)
- [3.8 Traceability Position](#)

This section does not replace any parent architecture, source architecture, logical profile, implementation source, or handbook. Instead, it identifies how the Phase 0 implementation profile preserves traceability to those architectural sources while mapping selected FX logical elements to Phase 0 implementation artifacts.

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