

3. Traceability to Parent and Source Architectures

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This part preserves [traceability](#) to SIP-RA, FDIS-RA, the Part 1 Conceptual Architecture, the Part 2 Distributed Node-Based Logical Architecture / [Platform-Independent Model \(PIM\)](#), the Part 3 FX Demo Logical Profile, Phase 0 implementation source material, and the Phase 0 Developer Handbook.

SIP-RA provides the parent structured-information-processing discipline. FDIS-RA provides financial-domain interpretation alignment. Part 1 provides the conceptual foundation. Part 2 provides the reusable distributed, node-based Logical Architecture / PIM. Part 3 provides the FX Demo Logical Profile. This part maps selected Part 3 FX logical elements to Phase 0 implementation artifacts. Phase 0 implementation source material provides the selected technology and repository context. The Phase 0 Developer Handbook defines the coding, file-header, generated-file, logging, exception-handling, naming, repository workflow, and review conventions that govern implementation work.

This part does not replace any parent architecture, source architecture, logical profile, or handbook. It defines the Phase 0 implementation mapping that connects selected FX logical elements to selected Phase 0 implementation artifacts while preserving [traceability](#) to the architectural sources governing those elements.

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