

3. Traceability to Parent and Source Architectures

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This section describes how the Phase 0 Implementation Profile / [Platform-Specific Model \(PSM\)](#) preserves [traceability](#) to SIP-RA, FDIS-RA, the Part 1 Conceptual Architecture, the Part 2 Distributed Node-Based [Platform-Independent Model \(PIM\)](#), the Part 3 FX Demo Logical Profile, the Phase 0 implementation source material, and the Phase 0 Developer Handbook.

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