

3.1 Traceability to SIP-RA

[Go To Traceability to Parent and Source Architectures](#)

SIP-RA supplies the parent architecture discipline for structured information processing systems. This part applies and specializes that discipline for the Phase 0 implementation of the FX Demo.

The Phase 0 Implementation Profile preserves the SIP-RA emphasis on structured information, explicit interfaces, separation of concerns, [traceable interpretation](#), governed processing, reviewability, and implementation discipline. It applies those concerns by mapping selected FX logical [Nodes](#), logical [Communication Endpoints](#), logical information structures, [Runtime Plane](#) classifications, interaction patterns, and traceability relationships to concrete Phase 0 implementation artifacts.

Table 3-1: SIP-RA concerns addressed by the Phase 0 Implementation Profile.

SIP-RA concern	Part 4: Phase 0 implementation treatment
Structured information processing	Maps selected FX logical information structures to IDL structures, generated types, and implementation-level data representations.
Explicit interfaces	Maps selected FX logical Communication Endpoints to DDS Topics or other selected communication artifacts.
Separation of concerns	Preserves the distinction among implementation artifacts that realize Control, Data, Health and Observability, Policy and Release, and Audit and Provenance.
Traceable interpretation	Preserves traceability from implementation artifacts to the FX logical elements, inherited logical elements, and conceptual elements they realize.
Governed processing across boundaries	Defines implementation mappings that support policy-controlled release and audit/provenance obligations.
Audit and review	Identifies implementation artifacts that support later deployment, observation, testability, audit, provenance, replay, reconstruction, and evidence planning.
Implementation discipline	Requires coding, logging, exception-handling, generation, file-header, and repository conventions through the Phase 0 Developer Handbook.

© 2026 Dido Solutions, Inc. and Jackrabbit Consulting, Inc.

From:
<https://wiki.didosolutions.com/> - Dido Solutions, Inc Wiki

Permanent link:
<https://wiki.didosolutions.com/fxdemo/04-part/03-traceability-to-parent-and-source-architectures/03-1-traceability-to-sip-ra/start>

Last update: 2026/07/22 18:59

