

P4-REQ-21-6-006

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Statement

Each IDL and generated type mapping SHALL identify the producing and consuming FX logical Nodes or Node Roles.

Source

Part 4, Section 21.6: Information Structure Mapping Requirements.

Rationale

Identifying the producing and consuming logical responsibilities makes clear which FX logical Nodes or Node Roles create and use the represented information.

This preserves the allocation of information responsibilities defined by the FX Demo Logical Profile and supports end-to-end communication traceability.

Applies To

This requirement applies to:

- IDL and generated type mappings;
- producing FX logical Nodes or Node Roles;
- consuming FX logical Nodes or Node Roles; and
- implementation participant mappings.

Verification

Verification SHALL confirm that each IDL and generated type mapping identifies its producing and consuming FX logical Nodes or Node Roles.

Verification activities should include review checks confirming that:

- producing logical responsibilities are identified;
- consuming logical responsibilities are identified;
- implementation participants map to the applicable logical responsibilities; and
- producer and consumer mappings agree with Part 3.

Traceability

This requirement connects Phase 0 information representations to the FX logical Nodes and Node Roles that produce and consume them.

Related requirement identifiers:

- [P4-REQ-21-6-005](#)
- [P4-REQ-21-4-003](#)
- [P4-REQ-21-5-005](#)
- [P4-REQ-21-5-006](#)

Related source section:

- [21.6 Information Structure Mapping Requirements](#)

Status

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