

P4-REQ-21-6-001

[Return to Requirements](#)

Statement

The Phase 0 Implementation Profile / PSM SHALL map selected FX logical information structures to IDL structures and generated types.

Source

Part 4, Section 21.6: Information Structure Mapping Requirements.

Rationale

Explicit mapping from FX logical information structures to IDL structures and generated types preserves the relationship between logical information meaning and its selected Phase 0 implementation representation.

Without this mapping, implementation data representations could become disconnected from the logical structures that justify them.

Applies To

This requirement applies to:

- selected FX logical information structures;
- Phase 0 IDL structures;
- generated types; and
- information-structure traceability records.

Verification

Verification SHALL confirm that selected FX logical information structures are mapped to Phase 0 IDL structures and generated types.

Verification activities should include review checks confirming that:

- selected logical information structures have identified IDL mappings;
- generated types derive from the applicable IDL structures;
- mappings are documented and reviewable; and

- implementation data representations remain traceable to their logical sources.

Traceability

This requirement establishes forward traceability from Part 3 FX logical information structures to their Phase 0 implementation representations.

Related requirement identifiers:

- [P4-REQ-21-6-002](#)
- [P4-REQ-21-6-003](#)
- [P4-REQ-21-6-010](#)

Related source section:

- [21.6 Information Structure Mapping Requirements](#)

Status

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