

P4-REQ-21-4-002

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Statement

Each implementation node artefact SHALL identify the FX logical Node or Nodes it realises.

Source

Part 4, Section 21.4: Logical Node Mapping Requirements.

Rationale

An implementation node artefact must identify the logical Node responsibilities it realizes so that its architectural purpose remains explicit.

This identification supports review, maintenance, change control, and backward traceability from implementation to logical architecture.

Applies To

This requirement applies to:

- implementation node modules;
- runtime participants;
- process entry points; and
- implementation node mapping records.

Verification

Verification SHALL confirm that each implementation node artefact identifies the FX logical Node or Nodes it realizes.

Verification activities should include review checks confirming that:

- the applicable FX logical Node is explicitly identified;
- multi-Node realizations identify every applicable Node;
- the mapping agrees with the Part 3 Node definitions; and
- the mapping is represented in applicable traceability records.

Traceability

This requirement establishes backward traceability from each implementation node artefact to its applicable FX logical Node definition.

Related requirement identifiers:

- [P4-REQ-21-4-001](#)
- [P4-REQ-21-4-003](#)
- [P4-REQ-21-4-009](#)

Related source section:

- [21.4 Logical Node Mapping Requirements](#)

Status

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