

P4-REQ-21-4-001

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Statement

The Phase 0 Implementation Profile / PSM SHALL map selected FX logical Nodes to implementation artefacts.

Source

Part 4, Section 21.4: Logical Node Mapping Requirements.

Rationale

Explicit mapping between FX logical Nodes and Phase 0 implementation artefacts preserves the connection between logical architectural responsibilities and their selected implementation realizations.

Without this mapping, reviewers could not determine which implementation artefacts realize the logical responsibilities defined by the FX Demo Logical Profile.

Applies To

This requirement applies to:

- selected FX logical Nodes;
- Phase 0 implementation node artefacts;
- implementation mapping records; and
- implementation traceability records.

Verification

Verification SHALL confirm that selected FX logical Nodes are mapped to the Phase 0 implementation artefacts that realize them.

Verification activities should include review checks confirming that:

- selected FX logical Nodes have identified implementation mappings;
- implementation artefacts identify their applicable logical source;
- mappings are documented and reviewable; and

- unmapped implementation responsibilities are not introduced into the baseline.

Traceability

This requirement establishes forward traceability from selected Part 3 FX logical Nodes to their Phase 0 implementation realizations.

Related requirement identifiers:

- [P4-REQ-21-4-002](#)
- [P4-REQ-21-4-003](#)
- [P4-REQ-21-4-006](#)

Related source section:

- [21.4 Logical Node Mapping Requirements](#)

Status

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