

P4-REQ-21-2-006

[Return to Requirements](#)

Statement

The Phase 0 Implementation Profile / PSM SHALL define the implementation mappings needed for the Phase 0 implementation baseline.

Source

Part 4, Section 21.2: Phase 0 Profile Requirements.

Rationale

The Phase 0 baseline requires explicit mappings between the FX logical architecture and its selected implementation realisations. These mappings establish how logical Nodes, Communication Endpoints, information structures, Runtime Planes, interaction patterns, and related concerns are represented in Phase 0.

Explicit mappings make the implementation baseline reviewable and prevent implementation relationships from remaining implicit.

Applies To

This requirement applies to:

- Phase 0 implementation mappings;
- implementation node mappings;
- communication mappings;
- information-structure mappings;
- Runtime Plane mappings; and
- supporting implementation traceability.

Verification

Verification SHALL confirm that the Phase 0 Implementation Profile / PSM defines the implementation mappings required by the selected Phase 0 baseline.

Verification activities should include review checks confirming that:

- selected FX logical elements have defined implementation mappings;
- implementation artefacts identify the elements they realise or support;
- mappings are sufficiently explicit for implementation and review; and
- mappings support forward and backward traceability.

Traceability

This requirement establishes the mapping structure connecting the Phase 0 implementation baseline to the FX Demo Logical Profile.

Related requirement identifiers:

- [P4-REQ-21-2-001](#)
- [P4-REQ-21-2-002](#)
- [P4-REQ-21-2-005](#)

Related source section:

- [21.2 Phase 0 Profile Requirements](#)

Status

Draft

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Last update: **2026/08/06 15:35**

