

P4-REQ-21-2-002

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Statement

The Phase 0 Implementation Profile / PSM SHALL map selected Part 3 FX Demo Logical Profile elements to Phase 0 implementation artefacts.

Source

Part 4, Section 21.2: Phase 0 Profile Requirements.

Rationale

The Phase 0 implementation must remain an explicit realisation of the FX Demo Logical Profile rather than an independent implementation design. Mapping selected Part 3 elements to implementation artefacts preserves the architectural relationship between logical meaning and implementation realisation.

Without explicit mappings, reviewers could not reliably determine which FX logical elements are realised by particular Phase 0 implementation artefacts.

Applies To

This requirement applies to:

- selected Part 3 FX logical elements;
- Phase 0 implementation artefacts;
- implementation mapping records; and
- implementation traceability artefacts.

Verification

Verification SHALL confirm that selected Part 3 FX Demo Logical Profile elements are mapped to the Phase 0 implementation artefacts that realise or support them.

Verification activities should include review checks confirming that:

- each selected implementation artefact identifies its applicable FX logical source;
- mappings identify the implementation realisation of selected logical elements;

- mappings are reviewable in both logical-to-implementation and implementation-to-logical directions; and
- implementation artefacts do not introduce untraceable logical responsibilities.

Traceability

This requirement establishes forward traceability from the Part 3 FX Demo Logical Profile to the Phase 0 implementation baseline.

Related requirement identifiers:

- [P4-REQ-21-2-003](#)
- [P4-REQ-21-2-005](#)
- [P4-REQ-21-2-006](#)

Related source section:

- [21.2 Phase 0 Profile Requirements](#)

Status

Draft

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Last update: **2026/08/06 15:34**

